

< Multi : 17.20% - 18.40% >

Dimension 156 mm x 156 mm ± 0.5 mm

Wafer Thickness 200µm ± 30µm
180µm + 30µm / -20µm

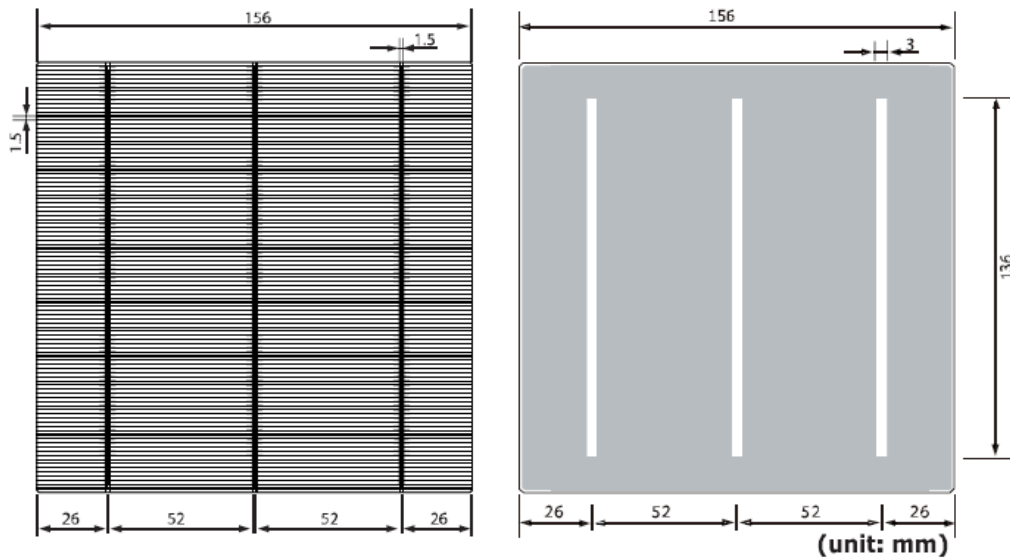
Front(-) Three 1.5 mm wide bus bars (silver) with distance 52 mm, acid texturized surface with blue silicon nitride AR coating.

Back(+) 3 mm wide silver/aluminum soldering pads, aluminum back surface field.

Efficiency code	1720	1740	1760	1780	1800	1820	1840
Efficiency (min) (%)	17.2	17.4	17.6	17.8	18.0	18.2	18.4
Pmax (W)	4.186	4.234	4.283	4.332	4.380	4.429	4.478
Voc (V)	0.631	0.632	0.634	0.636	0.638	0.640	0.642
Isc (A)	8.446	8.485	8.523	8.573	8.620	8.660	8.700
Vmp (V)	0.525	0.527	0.529	0.531	0.533	0.535	0.537
Imp (A)	7.973	8.035	8.097	8.160	8.220	8.280	8.340

*Data under standard testing conditions (STC): 1,000W/m², AM1.5, 25°C, Pmax: Positive power tolerance.

Voltage -2.08 mV/K
Current +4.58 mA/K
Power -0.40 %/K



(Cell Efficiency 17.8 %)

